For more awesome GCSE and A level resources, visit us at www.savemyexams.co.uk/

Electronics

Question paper 3

Level	International A Level
Subject	Physics
Exam Board	CIE
Topic	Electronics
Sub Topic	
Paper Type	Theory
Booklet	Question paper 3

Time Allowed: 71 minutes

Score: /59

Percentage: /100

A*	А	В	С	D	E	U
>85%	'77.5%	70%	62.5%	57.5%	45%	<45%

For more awesome GCSE and A level resources, visit us at www.savemyexams.co.uk/

1 (a) State three properties of an ideal operational amplifier (op-amp).

1.	
2	
۷.	
3	
٥.	[:

(b) A circuit incorporating an ideal op-amp is to be used to indicate whether a door is open or closed.

Resistors, each of resistance R, are connected to the inputs of the op-amp, as shown in Fig. 10.1.

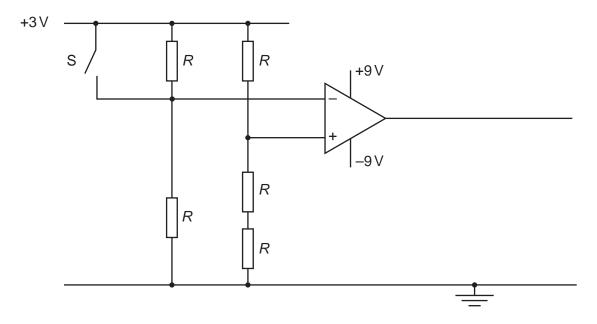


Fig. 10.1

The switch S is attached to the door so that, when the door is open, the switch is open. The switch closes when the door is closed.

(i)	Explain why the polarity of the output of the op-amp changes when the swi closes.	tch
		.[3]
(ii)	A red light-emitting diode (LED) is to be used to indicate when the door is open. A green LED is to indicate when the door is closed.	
	On Fig. 10.1,	
	 draw symbols for the LEDs to show how they are connected to the output of op-amp, 	the [1]
	2. identify the green LED with the letter G.	[1]

2 The circuit diagram of Fig. 9.1 is an amplifier circuit incorporating an operational amplifier (op-amp).

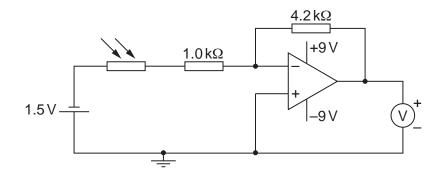


Fig. 9.1

(a)	(i)	On Fig. 9.1, mark, with the letter X, the virtual earth.	[1]
	(ii)	Explain what is meant by a virtual earth.	
			[3]

- **(b)** In bright sunlight, the light-dependent resistor (LDR) has resistance 200Ω .
 - (i) Calculate, for the LDR in bright sunlight, the voltmeter reading.

(ii)	The sunligh					_	•				
	State and brightness.	explain	the e	ffect on	the	voltmeter	reading	of t	his (decrease	in
	brightness.										
											• • • •
		• • • • • • • • • • • • • • • • • • • •									
											[3]

For more awesome GCSE and A level resources, visit us at www.savemyexams.co.uk/

	s. Stat	circuits.	amplifier	be used in	ς may	feedback	egative	a)	3
--	---------	-----------	-----------	------------	-------	----------	---------	----	---

(i)	what is meant by negative feedback,
	[2]
ii)	two effects of negative feedback on an amplifier incorporating an operational amplifier (op-amp).
	1
	2
	[2]

(b) Fig. 9.1 is a circuit for an amplifier that is used with a microphone.

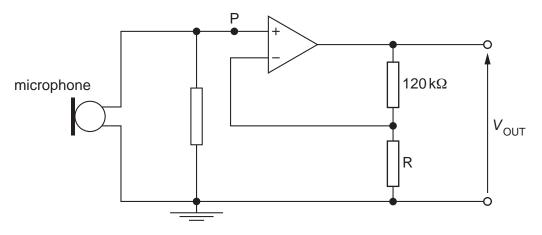


Fig. 9.1

The output potential difference $V_{\rm OUT}$ is 4.4V when the potential at point P is 62 mV. Determine

(i) the gain of the amplifier,

	(ii)	the resistance of the resistor R.
		resistance = Ω [2]
(c)		e maximum potential produced by the microphone at point P on Fig. 9.1 is 95 mV. e power supply for the operational amplifier may be either +/- 5 V or +/- 9 V.
	Sta	te which power supply should be used. Justify your answer quantitatively.
		[3]
	•••••	[6]

4 The circuit of Fig. 10.1 may be used to indicate temperature change.

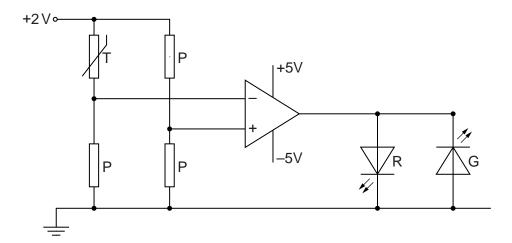


Fig. 10.1

The resistance of the thermistor T at $16\,^{\circ}\text{C}$ is $2100\,\Omega$ and at $18\,^{\circ}\text{C}$, the resistance is $1900\,\Omega$. Each resistor P has a resistance of $2000\,\Omega$.

Determine the change in the states of the light-emitting diodes R and G as the temperature of the thermistor changes from 16 °C to 18 °C.

5 An amplifier incorporating an operational amplifier (op-amp) has three inputs A, B and C, as

shown in Fig. 9.1.

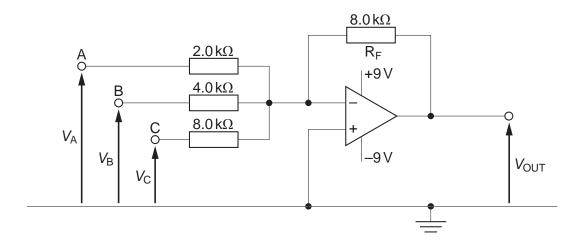


Fig. 9.1

Negative feedback is provided by the resistor R_{F} of resistance 8.0 k $\!\Omega.$

For each of the inputs A, B and C, the amplifier may be considered as a single input amplifier. That is, each input is independent of the other two.

When the amplifier is not saturated, the output potential V_{OUT} is given by the expression

$$V_{OUT} = -(4V_A + GV_B + V_C),$$

where $V_{\rm A}$, $V_{\rm B}$ and $V_{\rm C}$ are the input potentials of the inputs A, B and C respectively and G is a constant.

(a)	State two effects of negative feedback on an amplifier.
	1
	2

(b) In the expression for the output potential V_{OUT} , the constant G is the gain associated with input B. Show that the numerical value of G is 2.

[1]

(c) The input potentials $V_{\rm A}$, $V_{\rm B}$ and $V_{\rm C}$ are either zero or 1.0V.

The magnitudes of some output potentials for different combinations of $V_{\rm A}$, $V_{\rm B}$ and $V_{\rm C}$ are shown in Fig. 9.2.

V _A /V	V _B /V	V _C /V	V _{OUT} /V
0	0	1	1
0	1	0	
1	0	0	4
1	0	1	5
1	1	0	
1	1	1	

Fig. 9.2

(i)	Complete Fig. 9.2 for the three remaining values of V_{OUT} .	[1]
(ii)	Suggest a use for this circuit.	
		[1]

For more awesome GCSE and A level resources, visit us at www.savemyexams.co.uk/

6 (a) By reference to an amplifier, explain what is meant by negative feedback.

 			 		•••••	•••••	 	 	
 •••••			 •••••	************			 	 	
 	,	,	 			,	 	 	 [2]

(b) An amplifier circuit incorporating an ideal operational amplifier (op-amp) is shown in Fig. 10.1.

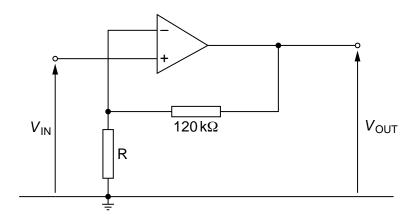


Fig. 10.1

The supply for the op-amp is ± 9.0 V. The amplifier circuit is to have a gain of 25.

Calculate the resistance of resistor R.

resistance =
$$\Omega$$
 [2]

(c) State the value of the output voltage V_{OUT} of the amplifier in (b) for input voltages V_{IN} of

(i)
$$-0.08$$
 V,

$$V_{OUT} = \dots V [1]$$

(ii) +0.4 V.

$$V_{OUT} = \dots V [1]$$

For more awesome GCSE and A level resources, visit us at www.savemyexams.co.uk/

7 (a) The circuit for an amplifier incorporating an ideal operational amplifier (op-amp) is shown in Fig. 10.1.

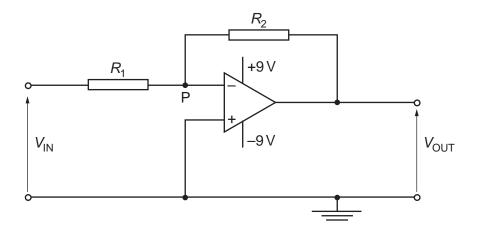


Fig. 10.1

- (i) State
 - 1. the name of this type of amplifier circuit,

	[1
--	---	---

2. why the point P is referred to as a *virtual earth*.

		\sim
	- 1	٠.
		J

(ii) Show that the gain G of this amplifier circuit is given by the expression

$$G = -\frac{R_2}{R_1}.$$

Explain your working.

For more awesome GCSE and A level resources, visit us at www.savemyexams.co.uk/

(b) The circuit of Fig. 10.1 is modified by connecting a light-dependent resistor (LDR) as shown in Fig. 10.2.

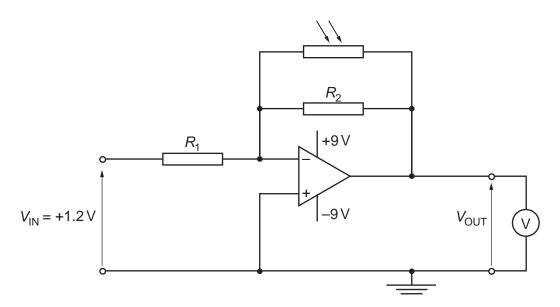


Fig. 10.2

The resistances R_1 and R_2 are $5.0\,\mathrm{k}\Omega$ and $50\,\mathrm{k}\Omega$ respectively. The input voltage V_IN is +1.2V. A high-resistance voltmeter measures the output V_OUT . The circuit is used to monitor low light intensities.

- (i) Determine the voltmeter reading for light intensities such that the LDR has a resistance of
 - **1.** 100 kΩ,

reading = V [3]

2. 10 kΩ.

(ii)	The light incident on the LDR is provided by a single lamp. Use your answers in (i) to describe and explain qualitatively the variation of the voltmeter reading as the lamp is moved away from the LDR.
	[3]